IN THE CLAIMS

1. (Currently Amended) A memory system for a portable telephone including a signal transmission/reception portion for transmitting and receiving a signal and a control portion for controlling at least a signal transmission and reception operation of said transmission/reception portion, comprising:

a random access memory providing a working area for said control portion; and

a flash memory including a memory array storing a program for said control portion and at least transmission and reception data in a non-volatile manner under a control of said control portion, said memory array being divided into a plurality of storage units, and a register, provided commonly to the respective storage units, having information in a storage unit of said plurality of storage units transmitted thereinto for temporal storage of the transmitted information and allowing serial readout of the transmitted and stored information.

2. (Previously Presented) The memory system for the portable telephone according to claim 1, wherein said random access memory and said flash memory are coupled to an internal bus interconnecting said control portion and said signal transmission/reception portion.

3. (Previously Presented) The memory system for the portable telephone according to claim 2, further comprising a bus converting circuit connected between said flash memory and said internal bus and functioning as an interface circuit for said flash memory.

4. (Previously Presented) The memory system for the portable telephone according to claim 3, wherein said flash memory and said bus converting circuit are integrally formed into a memory card being attachable and detachable to and from said portable telephone.

5. (Previously Presented) The memory system for the portable telephone according to claim 3, wherein said flash memory is constituted of a memory card being attachable and detachable to and from said bus converting circuit.

6. (Previously Presented) The memory system for the portable telephone according to claim 1, wherein said control portion, said random access memory and said flash memory are integrally formed as a control unit.

7. (Previously Presented) The memory system for the portable telephone according claim 1, wherein said flash memory comprises an AND type flash memory.

8. (Original) The memory system for the portable telephone according to claim 5, wherein said bus converting circuit is formed into an adapter attachable and detachable to said portable telephone.

9. (Previously Presented) The memory system for a portable telephone according to claim 1, wherein a program stored in the storage unit of said plurality of storage units is serially read out to the random access memory to be executed.

10. (Previously Presented) The memory system for a portable telephone according to claim 1, wherein said control portion performs a process using the random access memory as an instruction memory to which the program is serially transferred from the flash memory.

11. (Previously Presented) The memory system for a portable telephone according to claim 1, wherein said control portion stores transmission and reception data into said random access memory as a buffer memory, and transfers the stored transmission and reception data from the random access memory to the flash memory.

12. (Currently Amended) A memory system for a portable telephone including a signal transmission/reception portion transmitting and receiving a signal, and a control portion controlling at least a signal transmission and reception operation of said signal transmission/reception portion, comprising:

a random access memory providing a working area for said control portion; and

a flash memory including a memory array storing a program for said control portion and at least transmission and reception data in a non-volatile manner under a control of said control portion, said memory array being divided into a plurality of storage units, a plurality of pieces of information in one unit of the storage units selected in accordance with an address signal at a time, being allowed to be serially read out in synchronization with a clock signal without further address application.

13. (Previously Presented) The memory system for a portable telephone according to claim 12, wherein a program stored in the storage unit of said plurality of storage units is serially read out to the random access memory to be executed.

14. (Previously Presented) The memory system according to claim 12, wherein said control portion performs a process using the random access memory as an instruction memory to which the program is serially transferred from the flash memory.

15. (Previously Presented) The memory system according to claim 12, wherein said control portion stores transmission and reception data into said random access memory as a buffer memory, and transfers the stored transmission and reception data from the random access memory to the flash memory.

16. (Previously Presented) The memory system for a portable telephone according to claim 12, wherein one unit of the storage units comprises a storage capacity ranging from 512 bytes to 2 K bytes.

17. (Previously Presented) The memory system for a portable telephone according to claim 12, wherein the storage units are each formed of a sector.

18. (Previously Presented) The memory system for a portable telephone according to claim 1, wherein one unit of the storage units comprises a storage capacity ranging from 512 bytes to 2 K-- bytes.

19. (Previously Presented) The memory system for a portable telephone according to claim 1, wherein the storage units are each formed of a sector.